

	SEMI® International Standards	International Polished Wafer Task Force Moscone Center, San Francisco, CA Monday, Jul 8, 2019, 10 AM - 11 AM SEMICON West Standards Meetings, Moscone Center South Europe Leaders: Frank Riedel (Siltronic) Japan Leaders: Yasutoshi Takamori (GlobalWafers Japan) North America Leaders: John Valley (VSOP)	
		Agenda	
A.	ADMINISTRATIVE ISSUES		10:00 AM
	1) Introductions and Announcements	J. Valley	
	2) Anti Trust Reminder	J. Valley	
	3) Intellectual Property Policy	J. Valley	
	4) Effective Meeting Guidelines	J. Valley	
B.	REVIEW OF INTERNATIONAL MEETINGS		10:05 AM
	1) EU Review of recent activity	F. Riedel	
	2) Japan Review of recent activity	Y. Takamoti	
C.	Presentation		
	1. Introduction to QSSPC Metrology of Minority Carrier Lifetime and Bulk Iron Content in Polished Wafers	R. Sinton	10:10 AM
D.	Ballot Review		
	1. Doc. 6425 - Line Item Revision to M1-1017	J. Valley	10:20 AM
E.	Ballot Development		
	1. Line Item Revision to move Flatness and Shape Illustrations to Related Information	P. Wagner	10:30 AM
F.	Discussion		
	1. M20 review		10:50 AM
F.	ACTION ITEMS		
	1) From this meeting	J. Valley	10:55 AM
	2) Next meeting at SEMICON Europa, Nov 2019, Munich Germany		
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